

<b>V<sub>DSS</sub> , 40V</b> <b>R<sub>DS(ON)</sub> , 7.4mΩ (max.) @ V<sub>GS</sub>=10V</b> <b>R<sub>DS(ON)</sub> , 10mΩ (max.) @ V<sub>GS</sub>=4.5V</b> <b>I<sub>D</sub> , 56A</b>	<b>TO-252</b>	

Description	Features
<p>The SG40N05D uses advanced Trench technology and designs to provide excellent R<sub>DS(ON)</sub> with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.</p>	<ul style="list-style-type: none"> <li>• Low On-Resistance</li> <li>• Low Input Capacitance</li> <li>• Low Miller Charge</li> <li>• Low Input/Output Leakage</li> <li>• Pb-free lead plating; RoHS compliant</li> </ul>
	Applications
	<ul style="list-style-type: none"> <li>• Lithium-Ion Secondary Batteries</li> <li>• Load Switch</li> <li>• DC-DC converters and Off-line UPS</li> </ul>

### Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG40N05D	Halogen-Free	TO-252	D	Tape & Reel	2,500

### Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	40	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous <sup>Note 3</sup>	I <sub>D</sub>	T <sub>C</sub> =25°C	56
		T <sub>C</sub> =100°C	56
Drain Current-Pulsed <sup>Note 1</sup>	I <sub>DM</sub>	97	A
Drain Current-Continuous	I <sub>D</sub>	T <sub>A</sub> =25°C	14
		T <sub>A</sub> =70°C	10
Avalanche Current	I <sub>AS</sub>	24	A
Avalanche Energy, L=0.1mH	E <sub>AS</sub>	29	mJ
Maximum Power Dissipation	P <sub>D</sub>	T <sub>C</sub> =25°C	78
		T <sub>C</sub> =100°C	39
Storage Temperature Range	T <sub>STG</sub>	-55 to +175	°C
Operating Junction Temperature Range	T <sub>J</sub>	-55 to +175	°C

### Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Junction-to-Ambient <sup>Note 2</sup>	R <sub>θJA</sub>	Steady State	-	63.8	-	°C/W
Maximum Junction-to-Case	R <sub>θJC</sub>	Steady State	-	1.93	-	°C/W

## Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>DS</sub> =250μA	40	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =32V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250μA	1.2	-	2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>DS</sub> =8A	-	-	7.4	mΩ
		V <sub>GS</sub> =4.5V, I <sub>DS</sub> =6A	-	-	10.0	mΩ

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHz	-	1902	-	pF
Output Capacitance	C <sub>oss</sub>		-	242	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	122	-	
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	-	2.2	-	Ω

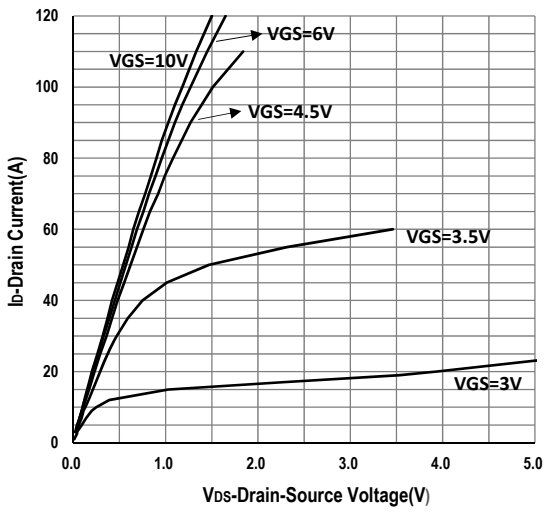
SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DD</sub> =15V, I <sub>D</sub> =19A, V <sub>GEN</sub> =10V, R <sub>GEN</sub> =3Ω	-	23.7	-	ns
Rise Time	t <sub>r</sub>		-	20.7	-	
Turn-Off Delay Time	T <sub>d(off)</sub>		-	24.7	-	
Fall Time	t <sub>f</sub>		-	16.8	-	
Total Gate Charge at 10V	Q <sub>g</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =19A	-	17.2	-	nC
Gate to Source Gate Charge	Q <sub>gs</sub>		-	7.4	-	
Gate to Drain "Miller" Charge	Q <sub>gd</sub>		-	5.9	-	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>DS</sub> =1A	-	-	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =19A, di/dt=100A/μs	-	21.2	-	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	14	-	nC

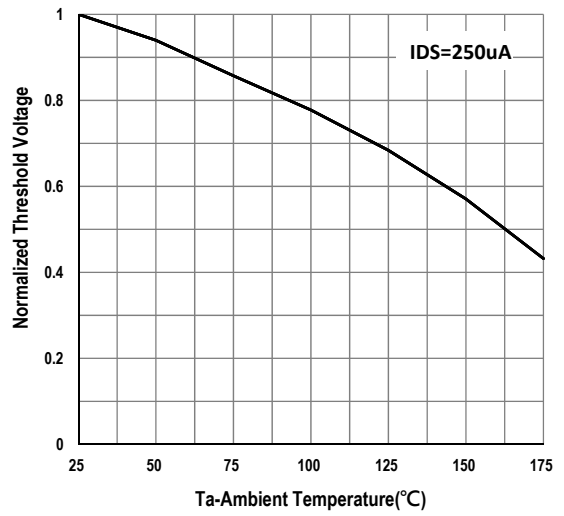
- Notes:**
1. Pulse Test: Pulse Width ≤ 10ms, Duty Cycle ≤ 1%.
  2. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>θJC</sub> is guaranteed by design while R<sub>θCA</sub> is determined by the user's board design. R<sub>θJA</sub> shown below for single device operation on FR-4 in still air.
  3. The maximum current rating is package limited.

Typical Operating Characteristics

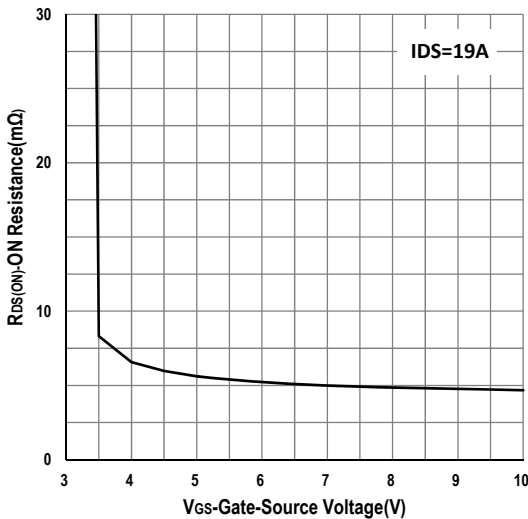
Output Characteristics



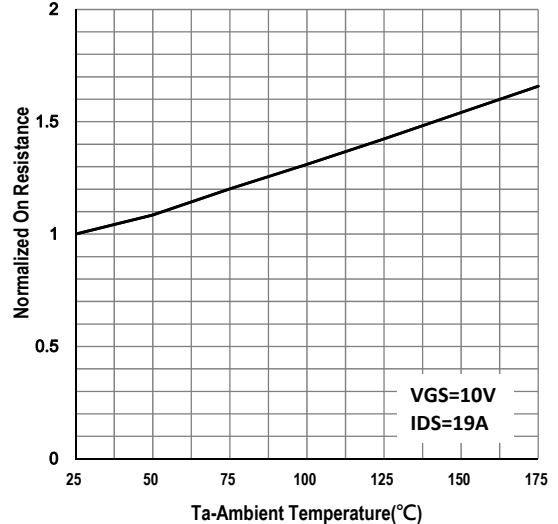
Gate Threshold Voltage



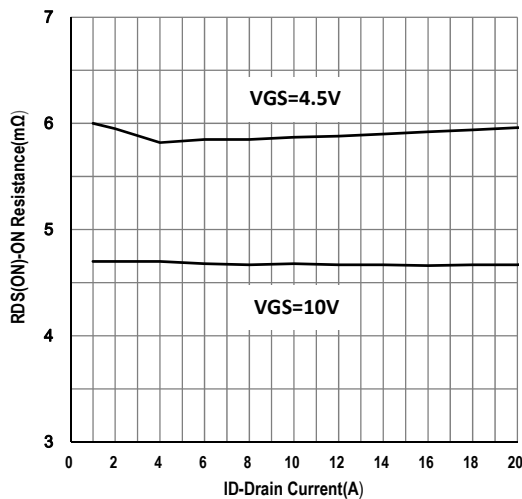
Gate-Source On Resistance



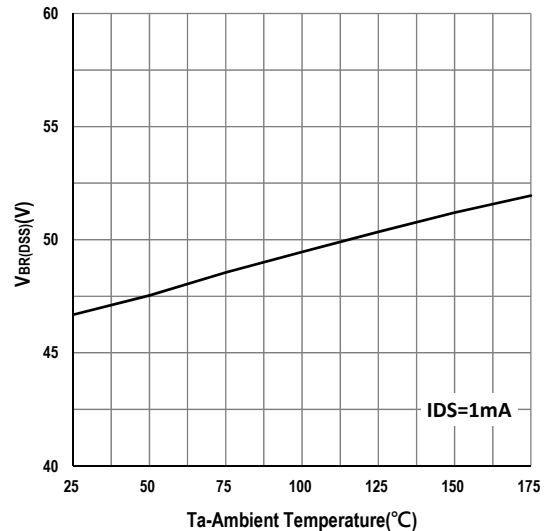
Drain-Source On Resistance



Drain-Source On Resistance

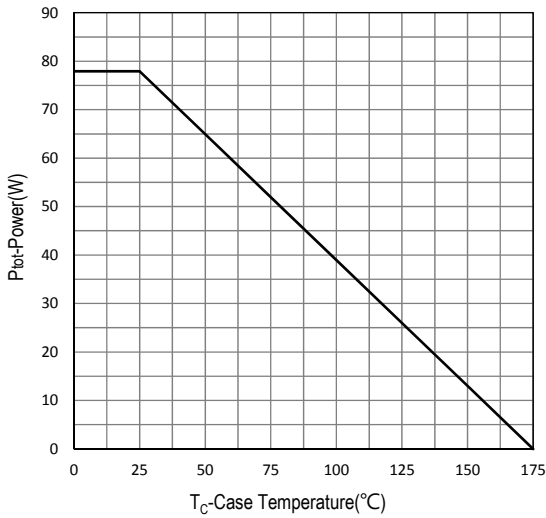


Drain-source Breakdown Voltage

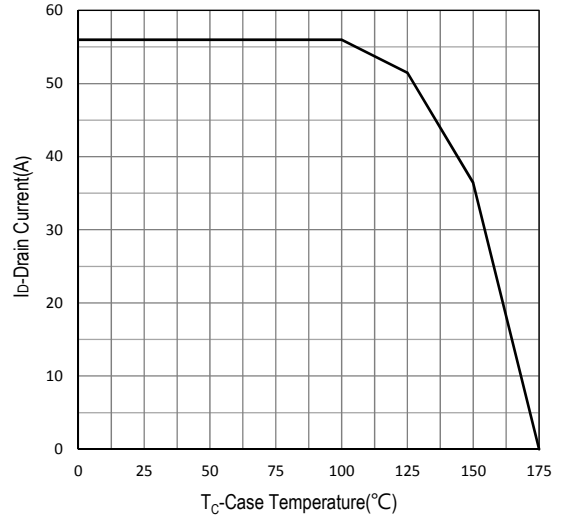


Typical Operating Characteristics (Cont.)

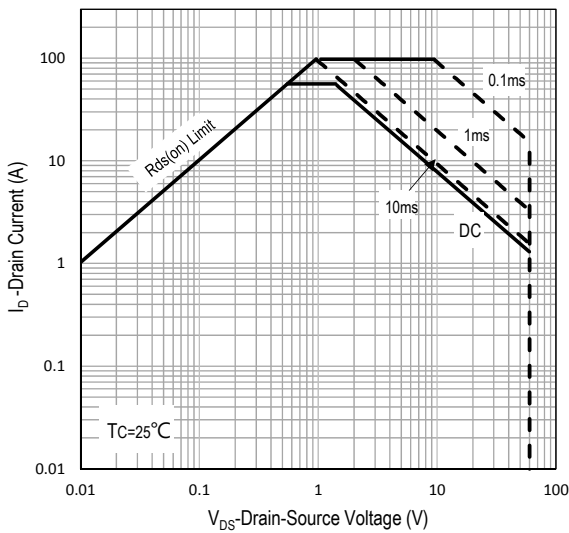
**Power Dissipation**



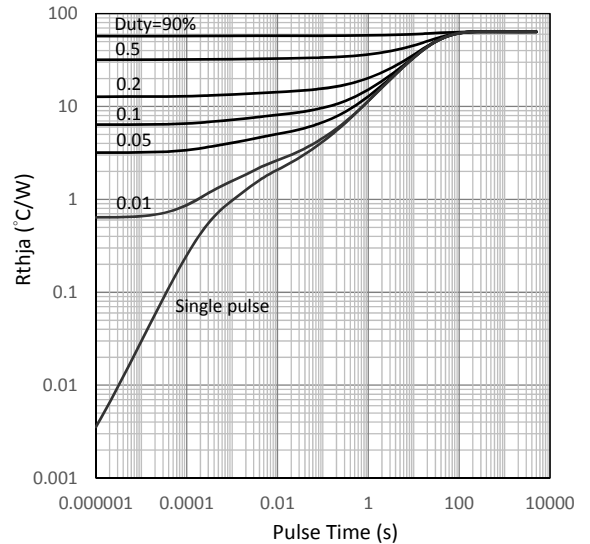
**Drain Current**



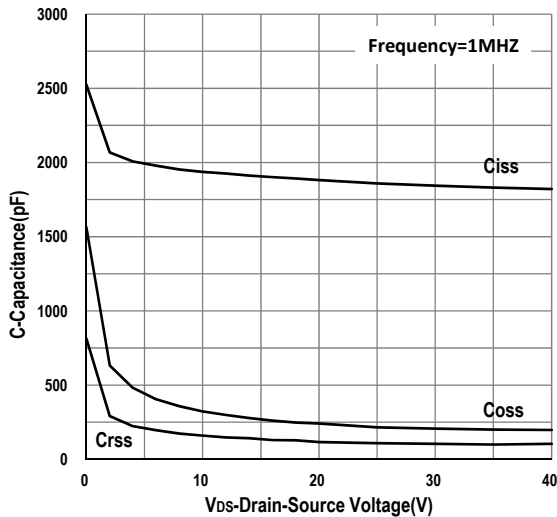
**Safe Operation Area**



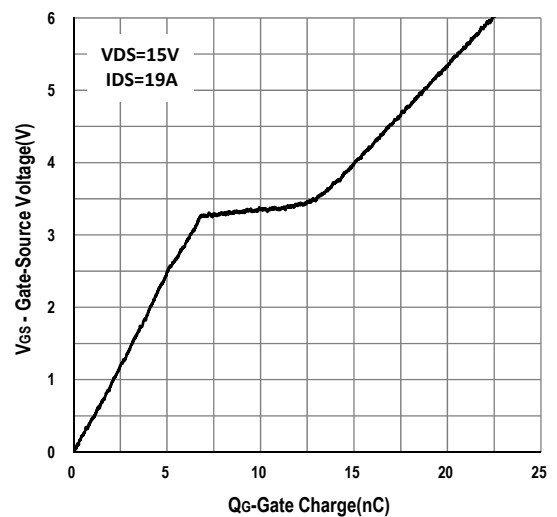
**Transient Thermal Impedance**



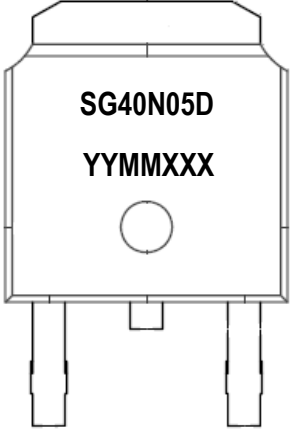
**Capacitance**



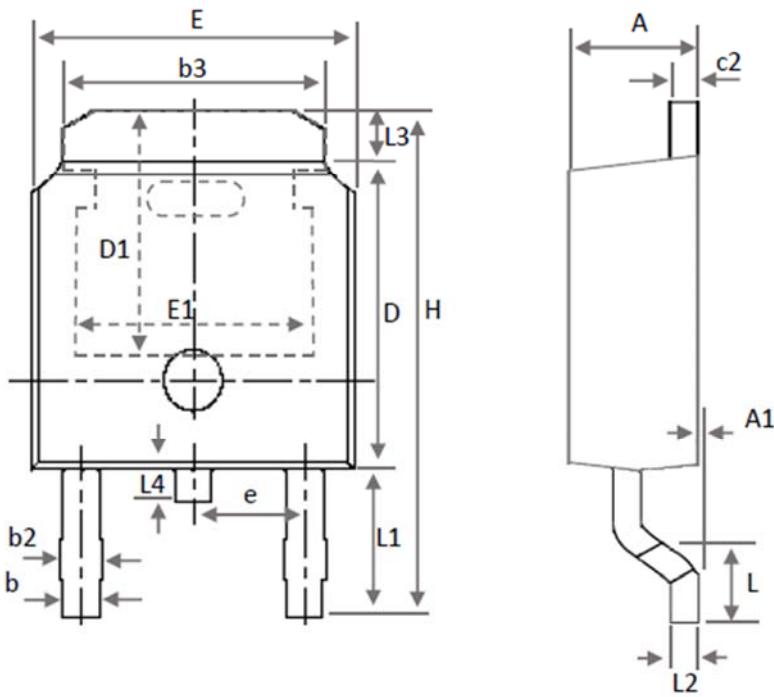
**Gate Charge**



**Marking Information**

TO-252 (D)	Marking Rule
<p>Laser Marking</p>  <p>Diagram</p>	<p><u>Line 1</u> : Device SG40N05D</p> <p><u>Line 2</u> : Date Code YYMMXXX</p> <p>YY : Year Code MM : Month Code XXX : Serial Number</p>

**Package of Dimension**

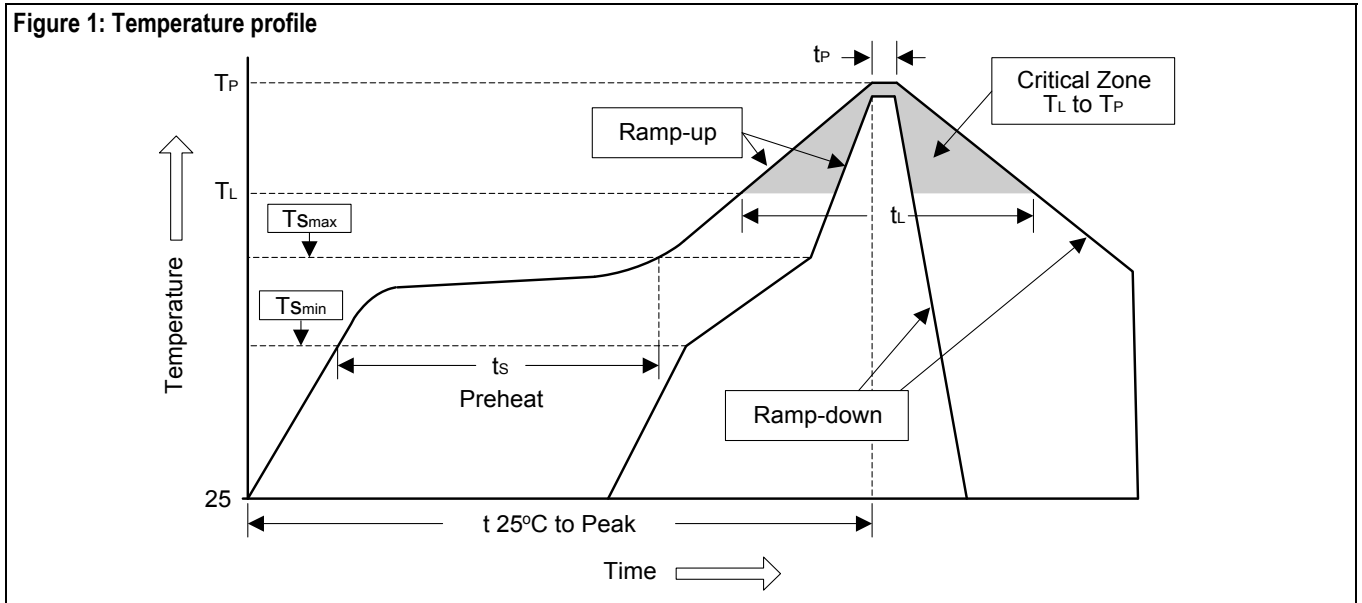


Symbol	Min	Nor	Max
E	6.35	6.54	6.731
L	1.40	1.59	1.78
L1	2.743 Ref.		
L2	0.508 BSC		
L3	0.89	1.08	1.27
L4	0.60	0.81	1.01
D	5.97	6.10	6.223
H	9.40	9.91	10.41
b	0.64	0.77	0.89
b2	0.76	0.95	1.14
b3	4.95	5.21	5.46
e	2.286 BSC		
A	2.18	2.29	2.39
A1	0.00	0.07	0.13
c2	0.46	0.68	0.89
D1	5.21	-	-
E1	4.32	-	-

## Soldering Methods for Silicongear's Products

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate ( $T_L$ to $T_P$ )	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min ( $T_{Smin}$ )	100°C	150°C
- Temperature Max ( $T_{Smax}$ )	150°C	200°C
- Time (min to max) ( $t_s$ )	60 to 120 sec	60 to 180 sec
$T_{Smax}$ to $T_L$		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature ( $T_L$ )	183°C	217°C
- Time ( $t_L$ )	60 to 150 sec	60 to 150 sec
Peak Temperature ( $T_P$ )	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature ( $t_P$ )	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

### 3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

## Important Notice

### © Silicongear Corporation

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Silicongear cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in an Silicongear product. No circuit patent licenses, copyrights, mask work rights, or other intellectual property rights are implied.

Silicongear Corporation, its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Silicongear"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Silicongear makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Silicongear disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Silicongear's knowledge of typical requirements that are often placed on Silicongear products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Silicongear's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Silicongear products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Silicongear product could result in personal injury or death. Customers using or selling Silicongear products not expressly indicated for use in such applications do so at their own risk and agree to fully indemnify and hold Silicongear and its distributors harmless from and against any and all claims, liabilities, expenses and damages arising or resulting in connection with such use or sale, including attorneys fees, even if such claim alleges that Silicongear or its distributor was negligent regarding the design or manufacture of the part. Please contact authorized Silicongear personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Silicongear. Product names and markings noted herein may be trademarks of their respective owners.

Silicongear and the Silicongear logo are trademarks of Silicongear Corporation. All other brand and product names appearing in this document are registered trademarks or trademarks of their respective holders.