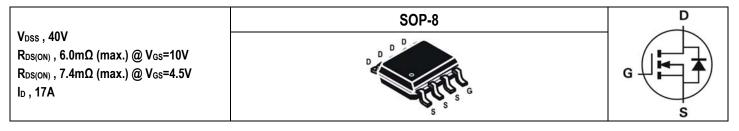


SG40N05S

40V N-Channel Power MOSFET



Description	Features
The SG40N05S uses advanced Trench technology and designs to provide excellent $R_{DS(ON)}$ with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.	 Low On-Resistance Low Input Capacitance Low Miller Charge Low Input / Output Leakage Pb-free lead plating; RoHS compliant
	Applications
	 Motor / Body Load Control Automotive Systems Load Switch
	 DC-DC converters and Off-line UPS

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG40N05S	Halogen-Free	SOP-8	S	Tape & Reel	3,000

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V _{DS}	40	V
Gate-Source Voltage		V _{GS}	±20	V
Drain Current-Continuous	T _A =25°C	L.	17	A
Drain Current-Continuous	T _A =70°C	l _D	14	А
Drain Current-Pulsed Note 1		I _{DM}	68	A
Avalanche Current		las	30	А
Avalanche Energy, L=0.1mH		E _{AS}	45	mJ
Maximum Dawar Dissinction	T _C =25°C	D	3.1	W
Maximum Power Dissipation	Tc=70°C		2	W
Storage Temperature Range		T _{STG}	-55 to +150	°C
Operating Junction Temperature Range		TJ	-55 to +150	°C

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Maximum Junction-to-Ambient	R _{0JA}	Steady State	-	-	75	°C/W
Maximum Junction-to-Ambient	Reja	t ≤ 10s	-	-	40	°C/W
Maximum Junction-to-Case	Rejc	Steady State	-	-	24	°C/W



Electrical Characteristics (TJ=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =250µA	40	-	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =32V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage	Igss	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250µA	1	-	2.5	V
Drain-Source On-State Resistance	D	V _{GS} =10V, I _{DS} =10A	-	-	6.0	mΩ
Drain-Source On-State Resistance	RDS(ON)	V _{GS} =4.5V, I _{DS} =7A	-	-	7.4	mΩ
Forward Transconductance Note 1	g fs	V _{DS} =5V, I _D =17A	-	25	-	S

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C _{iss}		-	3018	-	
Output Capacitance	Coss	V _{DS} =15V, V _{GS} =0V, f=1MHz	-	247	-	pF
Reverse Transfer Capacitance	Crss		-	183	-	
Gate Resistance	Rg	V_{GS} =0V, V_{DS} =0V, f=1MHz	-	1.5	-	Ω

SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Turn-On Delay Time	T _{d(on)}		-	18.2	-	
Rise Time	tr	V _{DD} =15V, I _D =17A, V _{GS} =10V,	-	9.8	-	
Turn-Off Delay Time	T _{d(off)}	Rg=3.3Ω	-	82.5	-	ns
Fall Time	tr		-	6.5	-	
Total Gate Charge	Qg		-	25	-	
Gate to Source Gate Charge	Q _{gs}	V _{DS} =20V, I _{DS} =15A, V _{GS} =4.5V	-	7	-	nC
Gate to Drain "Miller" Charge	Q_{gd}		-	11	-	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Maximum Body-Diode Continuous Current	ls	V _G =V _D =0V, Force Current	-	-	17	Α
Pulsed Source Current	I _{SM}	V _G =V _D =0V, Force Current	-	-	68	Α
Drain-Source Diode Forward Voltage	Vsd	V _{GS} =0V, I _S =17A	-	-	1.2	V

Notes:

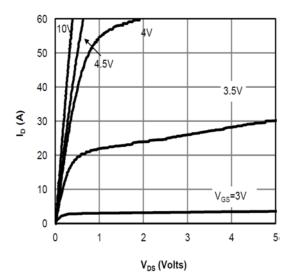
1. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

 R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BJC} is guaranteed by design while R_{BJA} is determined by the user's board design. R_{BJA} shown below for single device operation on FR-4 in still air.

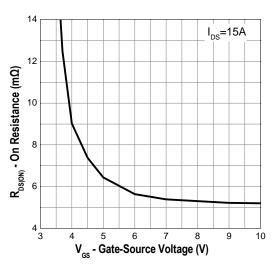


Typical Operating Characteristics

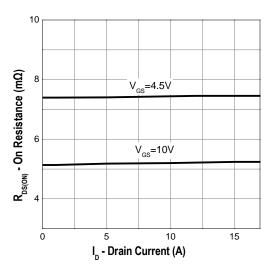
Output Characteristics



Gate-Source On Resistance

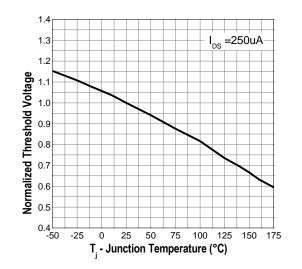


Drain-Source On Resistance

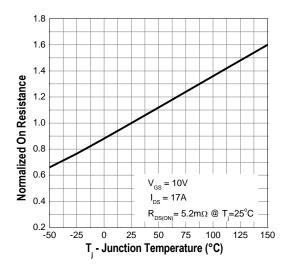




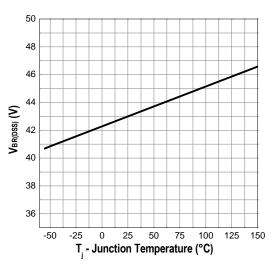
Gate Threshold Voltage



Drain-Source On Resistance



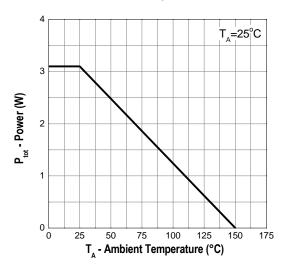
Drain-source Breakdown Voltage



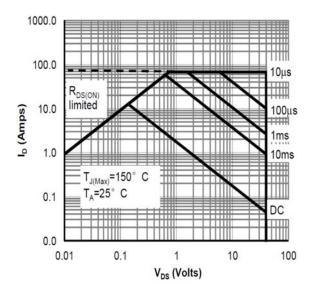


Typical Operating Characteristics (Cont.)

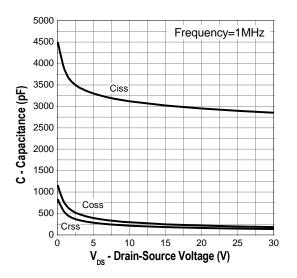
Power Dissipation

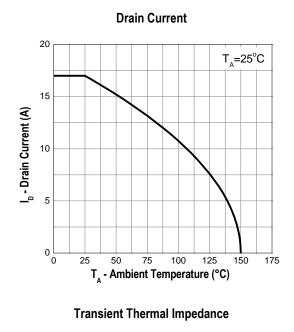


Safe Operation Area



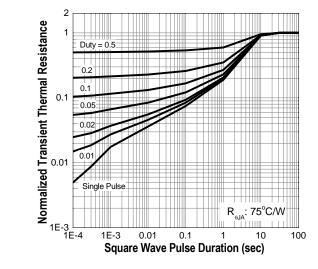
Capacitance



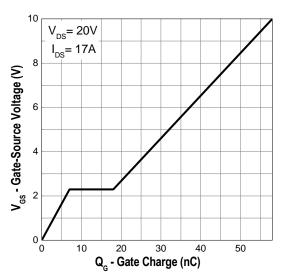


SG40N05S

40V N-Channel Power MOSFET



Gate Charge



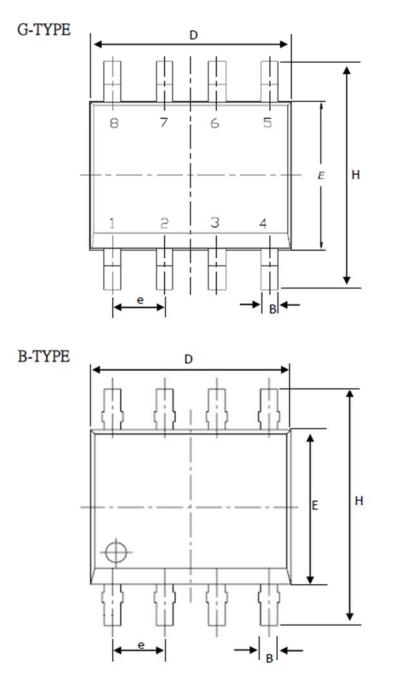


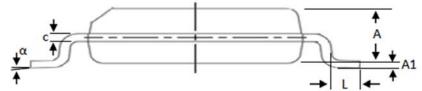
Marking Information

SOP-8 (S)	Marking Rule
Laser Marking SG40N05S	Line 1 : Device Name SG40N05S Line 2 : Date Code YYMMXXX
	YY:Year Code MM:Month Code XXX:Serial Number



Package of Dimension



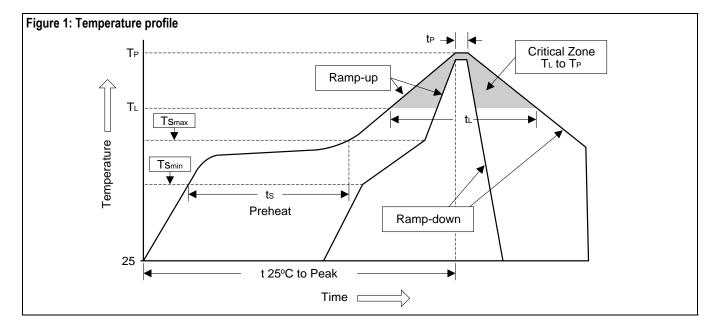


Symbol	Min	Nor	Max
Α	1.35	1.55	1.75
A1	0.10	0.18	0.25
B	0.31	0.41	0.51
С	0.17	0.21	0.25
D	4.80	4.90	5.00
E	3.80	3.90	4.00
е	1.27	1.27	1.27
Η	5.80	6.00	6.20
L	0.40	0.84	1.27
α	0.00	4.00	8.00



Soldering Methods for Silicongear's Products

- 1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
- 2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate $(T_L \text{ to } T_P)$	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (Ts _{min})	100°C	150°C
- Temperature Max (Ts _{max})	150°C	200°C
- Time (min to max) (ts)	60 to 120 sec	60 to 180 sec
Tsmax to T∟		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T∟)	183°C	217°C
- Time (t _L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T _P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak	10 to 30 sec	20 to 40 sec
Temperature (t _P)		2010-10300
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec





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