

V_{DSS} , 60V R_{DS(ON)} , 12mΩ (max.) @ V_{GS}=10V R_{DS(ON)} , 14.5mΩ (max.) @ V_{GS}=4.5V I_D , 63A	TO-220AB	

Description The SG60N10P uses advanced Trench technology and designs to provide excellent R _{DS(ON)} with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.	Features <ul style="list-style-type: none"> • Low On-Resistance • Low Input Capacitance • Low Miller Charge • Low Input / Output Leakage • Pb-free lead plating; RoHS compliant
	Applications <ul style="list-style-type: none"> • Motor / Body Load Control • Automotive Systems • Load Switch • DC-DC converters and Off-line UPS

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG60N10P	Halogen-Free	TO-220AB	P	Tube	50

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	T _C =25°C	63
		T _C =100°C	40
Drain Current-Pulsed <small>Note 1</small>	I _{DM}	165	A
Avalanche Current	I _{AS}	60	A
Avalanche Energy, L=0.1mH	E _{AS}	180	mJ
Maximum Power Dissipation	P _D	T _C =25°C	86.8
		T _C =100°C	34.7
Storage Temperature Range	T _{STG}	-55 to +150	°C
Operating Junction Temperature Range	T _J	-55 to +150	°C

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Junction-to-Ambient	R _{θJA}	Steady State	-	-	62	°C/W
Maximum Junction-to-Case	R _{θJC}	Steady State	-	-	1.44	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =48V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250μA	1.2	-	2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _{DS} =18A	-	-	12	mΩ
Drain-Source On-State Resistance		V _{GS} =4.5V, I _{DS} =10A	-	-	14.5	mΩ
Forward Transconductance ^{Note 1}	g _{fs}	V _{DS} =10V, I _D =30A	-	11	-	S

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C _{iss}	V _{DS} =30V, V _{GS} =0V, f=1MHz	-	2037	-	pF
Output Capacitance	C _{oss}		-	160	-	
Reverse Transfer Capacitance	C _{rss}		-	78	-	
Gate Resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	0.6	-	Ω

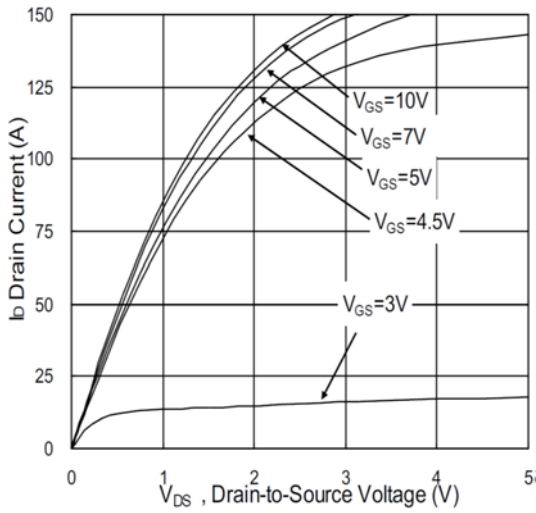
SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Turn-On Delay Time	T _{d(on)}	V _{DD} =30V, I _D =30A, V _{GS} =10V, R _g =3.3Ω	-	9.3	-	ns
Rise Time	t _r		-	27.4	-	
Turn-Off Delay Time	T _{d(off)}		-	43.9	-	
Fall Time	t _f		-	10.6	-	
Total Gate Charge	Q _g	V _{DS} =30V, I _{DS} =30A, V _{GS} =10V	-	38	-	nC
Gate to Source Gate Charge	Q _{gs}		-	5.7	-	
Gate to Drain "Miller" Charge	Q _{gd}		-	8.5	-	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Body-Diode Continuous Current	I _S	V _G =V _D =0V, Force Current	-	-	63	A
Pulsed Source Current	I _{SM}	V _G =V _D =0V, Force Current	-	-	165	A
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =30A	-	-	1.2	V

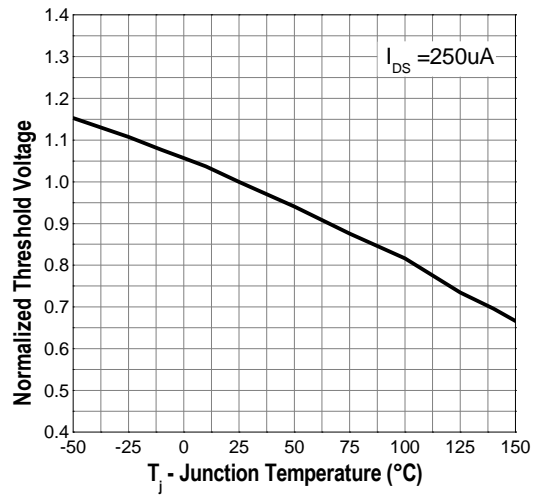
- Notes:**
- Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
 - R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{θJC} is guaranteed by design while R_{θJA} is determined by the user's board design. R_{θJA} shown below for single device operation on FR-4 in still air.

Typical Operating Characteristics

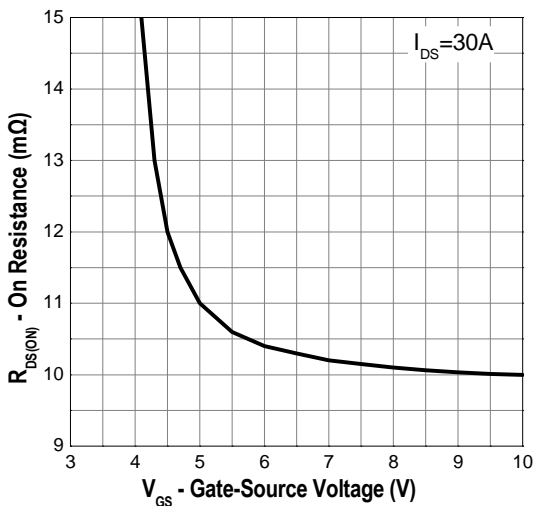
Output Characteristics



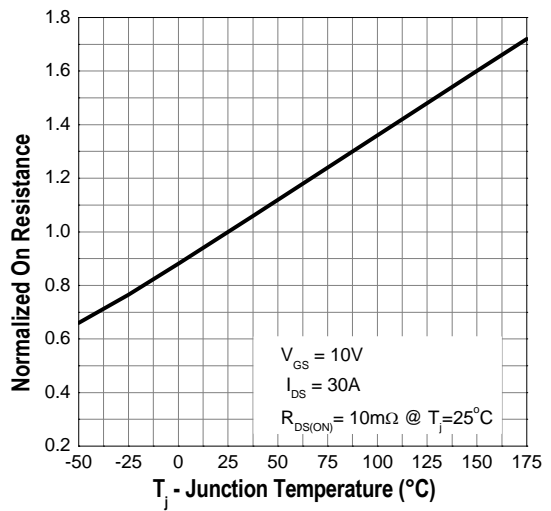
Gate Threshold Voltage



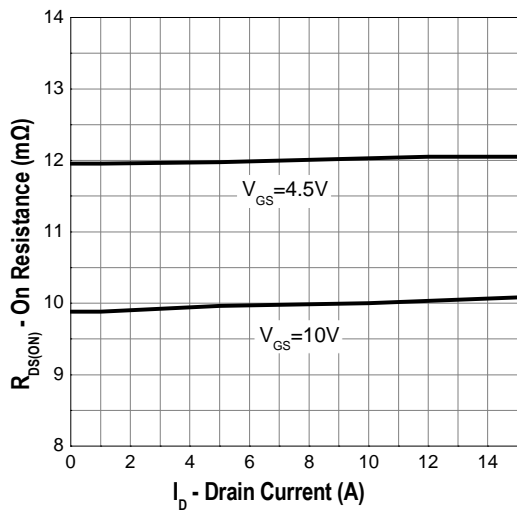
Gate-Source On Resistance



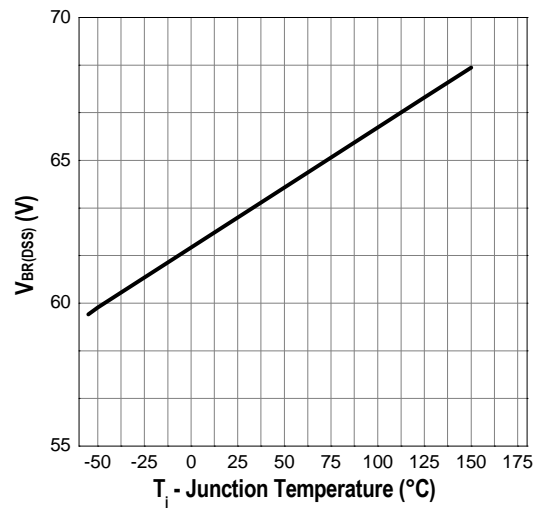
Drain-Source On Resistance



Drain-Source On Resistance

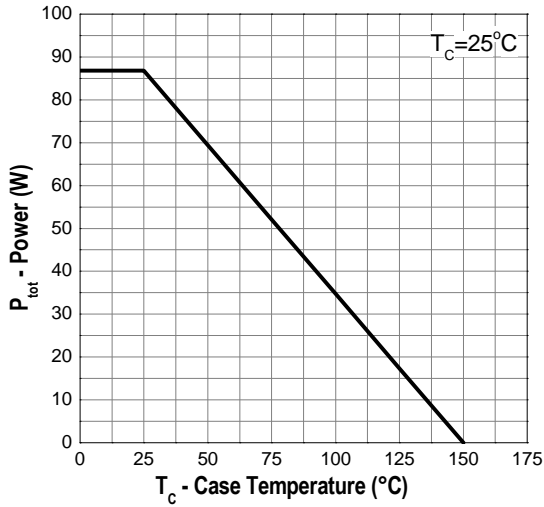


Drain-source Breakdown Voltage

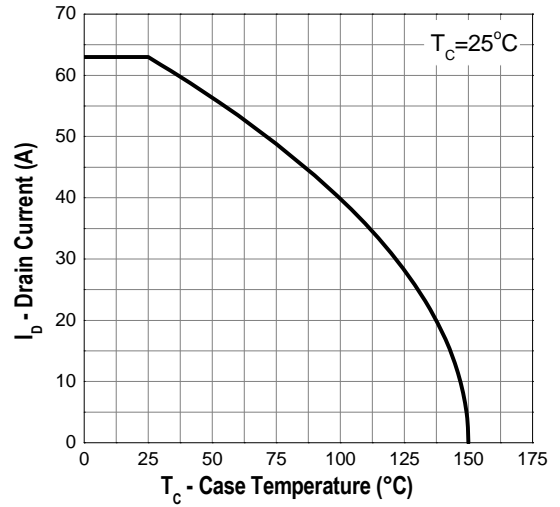


Typical Operating Characteristics (Cont.)

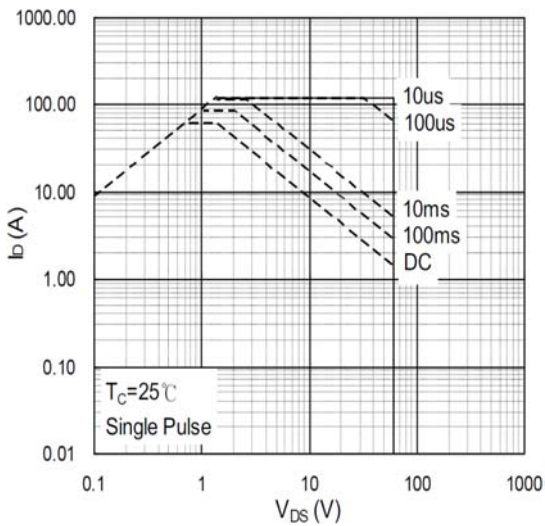
Power Dissipation



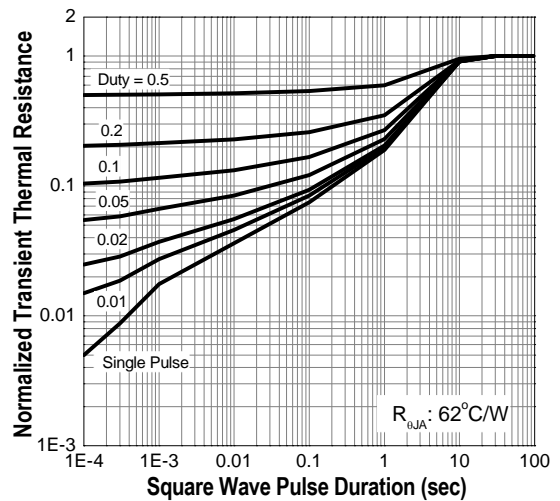
Drain Current



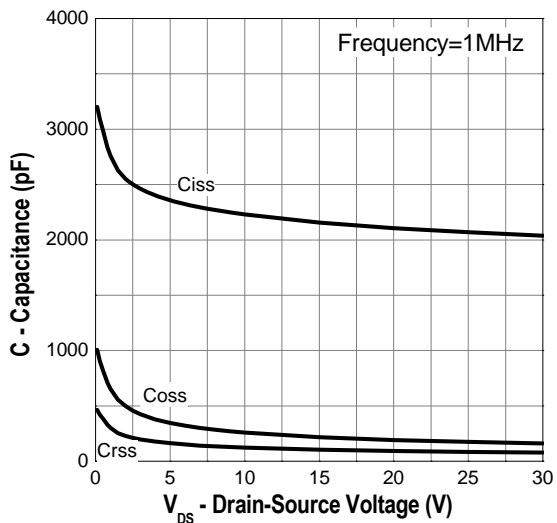
Safe Operation Area



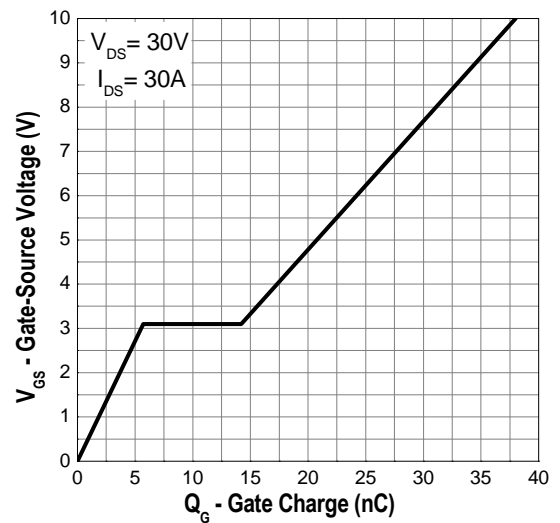
Transient Thermal Impedance



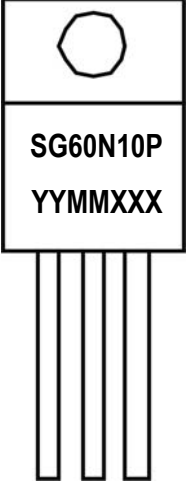
Capacitance



Gate Charge

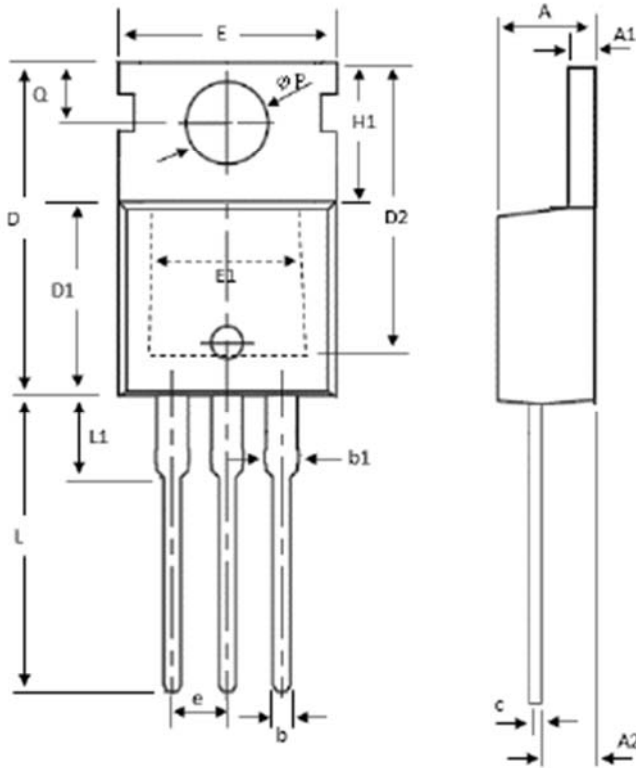


Marking Information

TO-220AB (P)	Marking Rule
<p>Laser Marking</p> 	<p><u>Line 1</u> : Device SG60N10P</p> <p><u>Line 2</u> : Date Code YYMMXXX</p> <p>YY : Year Code MM : Month XXX : Serial Number</p>

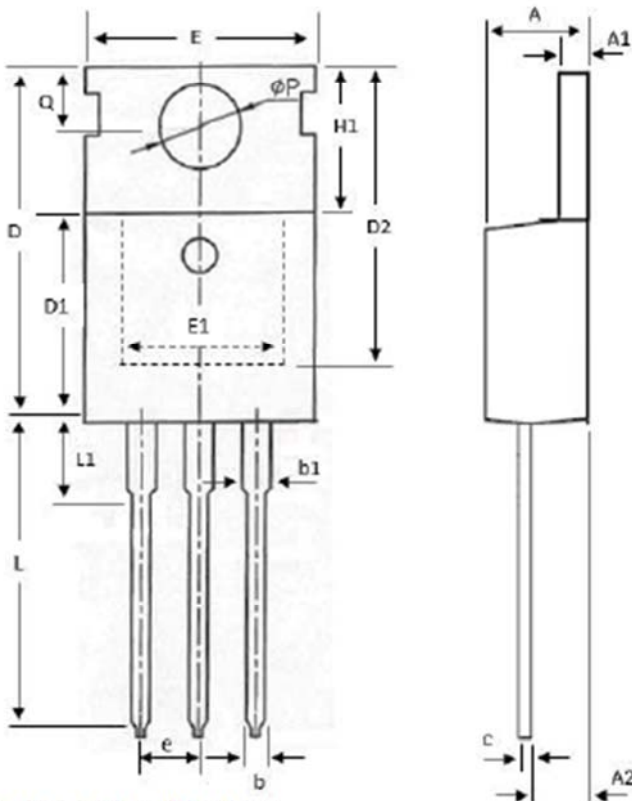
Package of Dimension

G-TYPE



Symbol	Min	Nor	Max
A	4.20	4.45	4.70
A1	1.15	1.28	1.40
A2	2.20	2.45	2.70
b	0.70	0.83	0.95
b1	1.15	1.45	1.75
c	0.40	0.50	0.60
D1	8.80	9.10	9.40
D2	11.75	-	-
E	9.70	10.03	10.36
E1	6.86	-	-
e	2.54 BSC		
H1	6.25	6.55	6.85
L	12.75	13.38	14.00
L1	-	-	4.00
P	3.40	3.70	4.00
Q	2.60	2.80	3.00

P-TYPE
H-TYPE

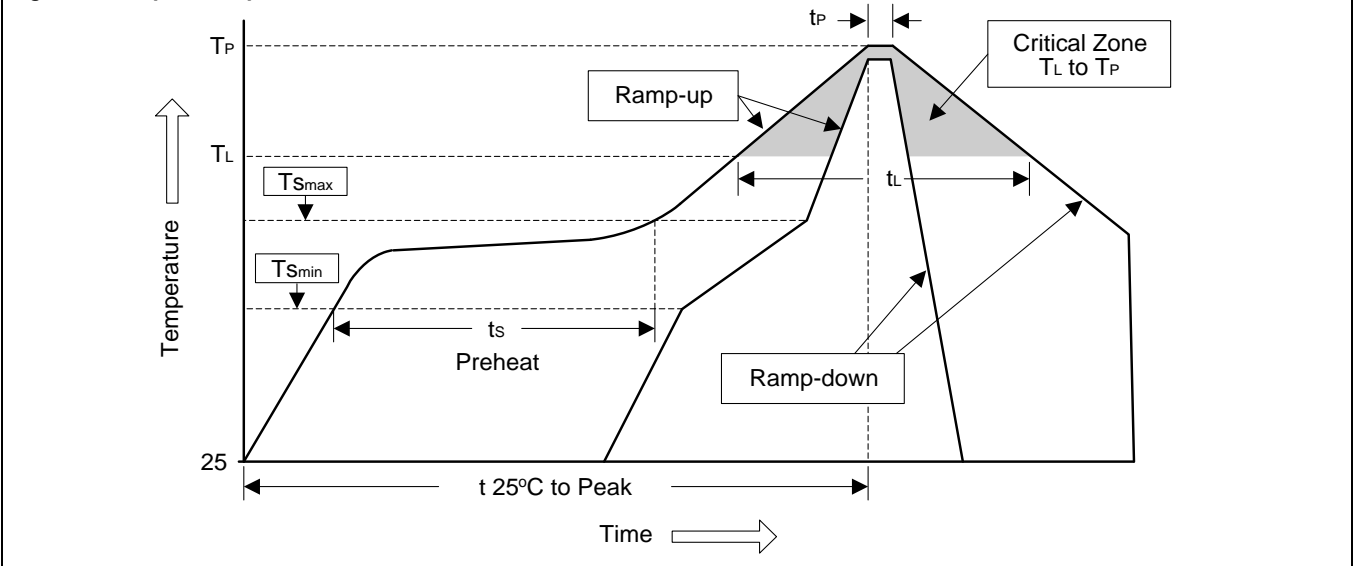


1. All dimension are in millimeters.
2. Dimension does not include burrs and mold flash/protrusions.

Soldering Methods for Silicongear's Products

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60 to 120 sec	60 to 180 sec
T_{Smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

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