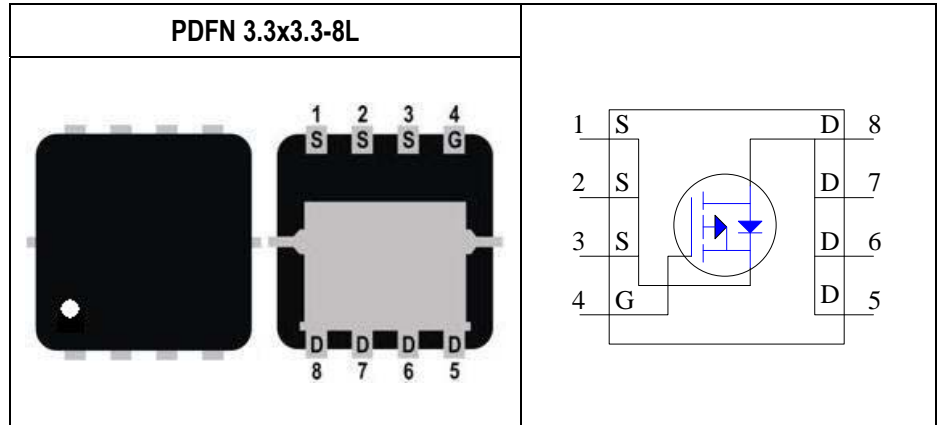


Parameter	Value	Unit
V_{DS}	-40	V
$R_{DS(ON) max. V_{GS}=10V}$	27	m Ω
$R_{DS(ON) max. V_{GS}=4.5V}$	40	m Ω
I_D	-15.8	A
$Q_{g 10V}$	26.4	nC
Q_{gd}	3.9	nC
Q_{SW}	7.2	nC



Features	Application
<ul style="list-style-type: none"> Low On-Resistance $R_{DS(on)}$ Low Input Capacitance Low Gate Charge Fully Characterized Capacitance and Avalanche Pb-free lead plating; RoHS compliant 	<ul style="list-style-type: none"> Load Switch Application Motor Driving Application Switch Mode Power Supply High Speed Switch Application With Logic Level Driving Application

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SGP4028E	Halogen-Free	PDFN 3.3x3.3-8L	E	Tape & Reel	5,000

Absolute Maximum Ratings ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-40	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ^{Note 1}	I_D	$T_C=25^\circ\text{C}$	-15.8
		$T_C=100^\circ\text{C}$	-9.9
Drain Current-Continuous ^{Note 2}	I_D	$T_A=25^\circ\text{C}$	-5
		$T_A=70^\circ\text{C}$	-3.7
Drain Current-Pulsed ^{Note 3}	I_{DM}	-40	A
Avalanche Current	I_{AR}	-17.6	A
Single Pulse Avalanche Energy ^{Note 4}	E_{AS}	15.5	mJ
Maximum Power Dissipation	P_D	$T_C=25^\circ\text{C}$	10
		$T_C=100^\circ\text{C}$	4
		$T_A=25^\circ\text{C}$	1
		$T_A=70^\circ\text{C}$	0.66
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Thermal resistance, Junction-Case ^{Note 5}	$R_{\theta JC}$	Steady State	-	-	12.44	$^\circ\text{C/W}$
Thermal resistance, Junction-Ambient ^{Note 5}	$R_{\theta JA}$	Steady State	-	-	120	$^\circ\text{C/W}$

Notes:

- Limited by silicon chip capability and $R_{\theta JC}$ junction-to-case thermal resistance.
- The maximum current rating is limited by package and $R_{\theta JA}$ junction-to-ambient thermal resistance.
- Must be ensure junction temperature does not exceed 150-degree C. (Pulse Width $\leq 100\mu\text{s}$, Duty $\leq 2\%$)
- Limited by T_{Jmax} , starting $T_J=25^\circ\text{C}$, $L=0.1\text{mH}$, $R_g=25\Omega$, $I_D=-17.6\text{A}$, $V_{GS}=-10\text{V}$.
- The value of thermal resistance is measured with the single device mounted on 1 inch² FR-4 PCB with 2 oz. copper under a still air environment temperature is 25°C based on JEDEC standard JESD51-14 and JESD51-2a. Thermal resistance obtained depends on the user's specific board design and given application.

Electrical Characteristics (T_J=25°C unless otherwise noted)

STATIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_{DS}=250\mu A$	-40	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-40V, V_{GS}=0V$	-	-	-1	μA
		$V_{DS}=-40V, V_{GS}=0V, T_J=125^\circ C$	-	-	-100	μA
Gate-Body Leakage	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA

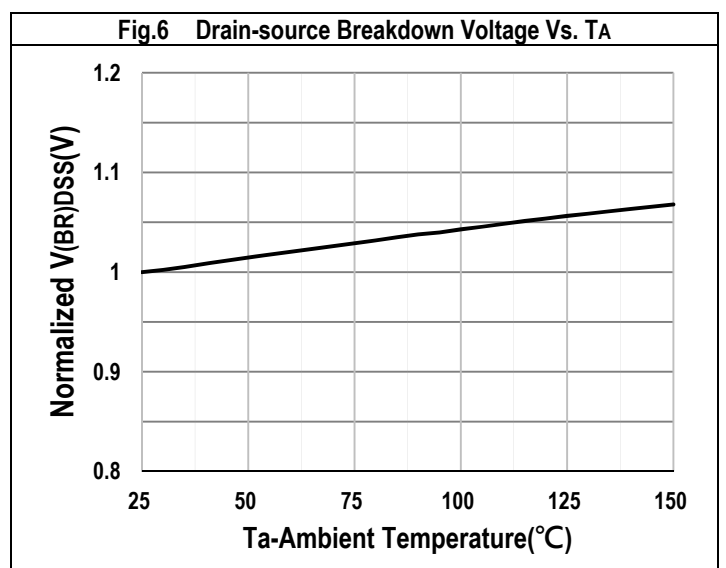
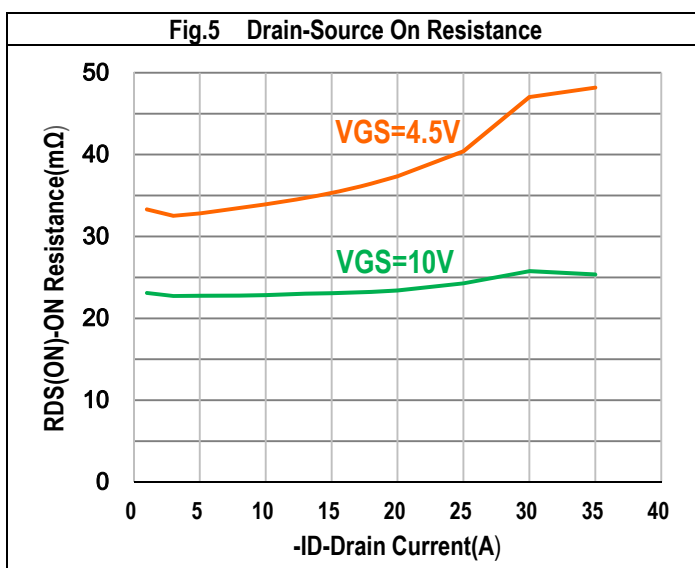
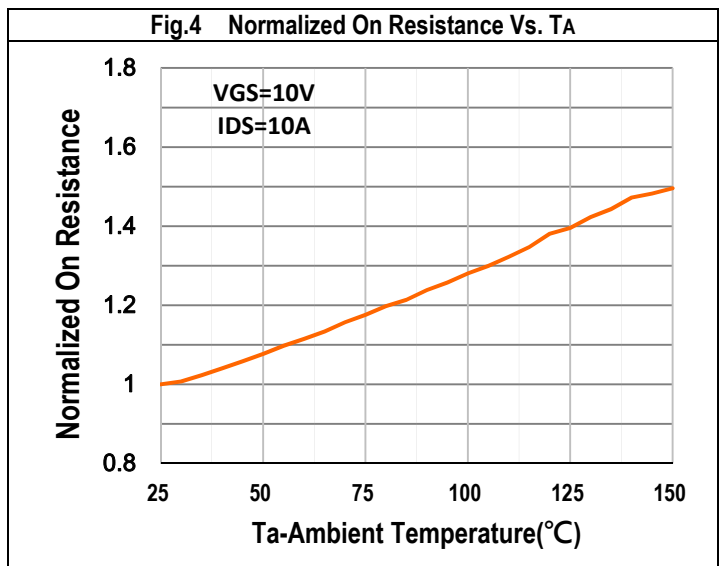
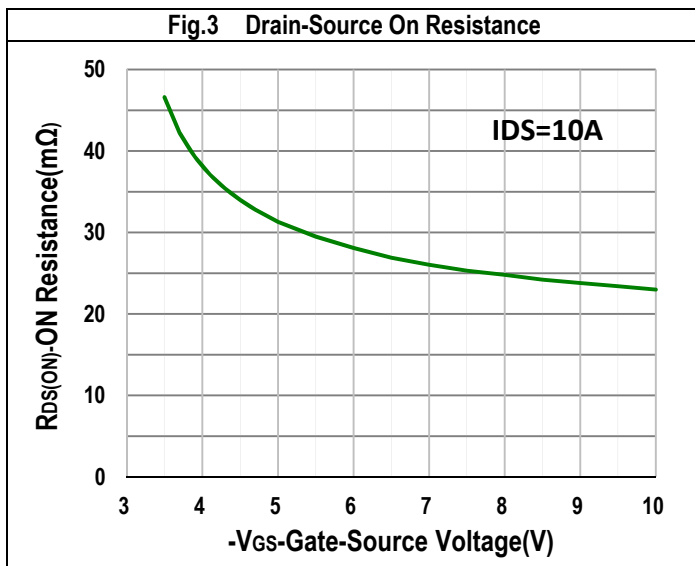
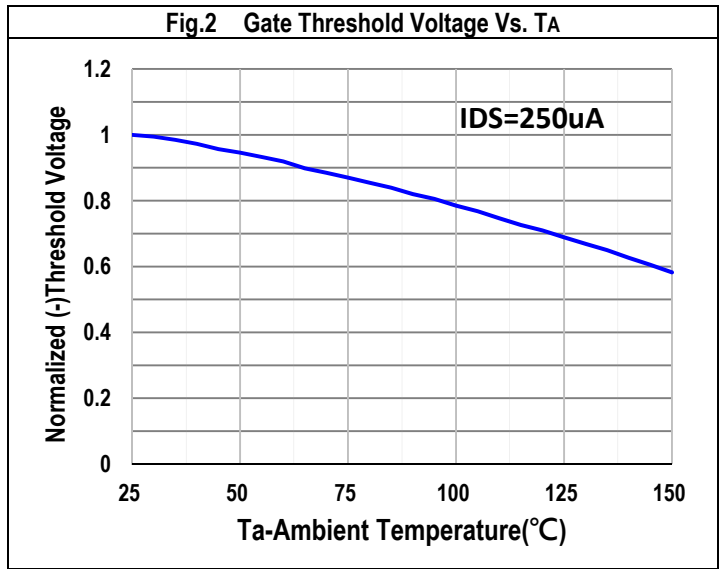
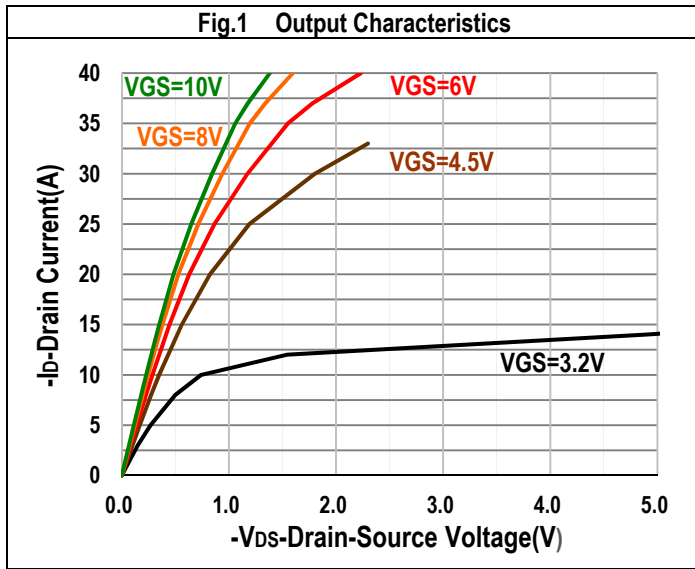
STATIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	-1.3	-1.6	-1.9	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_{DS}=-10A$	-	23	27	m Ω
		$V_{GS}=-4.5V, I_{DS}=-5A$	-	34	40	m Ω
Gate Resistance	R_g	$V_{GS}=0V, V_{DS}=0V, f=1MHz$	-	11	-	Ω
Forward Transconductance	g_{fs}	$V_{DS}=-5V, I_{DS}=-10A$	-	10	-	S

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C_{iss}	$V_{DD}=-40V, V_{DS}=-20V, V_{GS}=0V, f=1MHz$	-	1486	-	pF
Output Capacitance	C_{oss}	$V_{DD}=-40V, V_{DS}=-20V, V_{GS}=0V, f=1MHz$	-	115	-	pF
Reverse Transfer Capacitance	C_{rss}	$V_{DD}=-40V, V_{DS}=-20V, V_{GS}=0V, f=1MHz$	-	92	-	pF
Turn-On Delay Time	$T_{d(on)}$	$V_{DS}=-20V, V_{GS}=-10V, I_{DS}=-10A, R_{GEN}=6.8\Omega$	-	6.3	-	nS
Rise Time	t_r	$V_{DS}=-20V, V_{GS}=-10V, I_{DS}=-10A, R_{GEN}=6.8\Omega$	-	22.7	-	nS
Turn-Off Delay Time	$T_{d(off)}$	$V_{DS}=-20V, V_{GS}=-10V, I_{DS}=-10A, R_{GEN}=6.8\Omega$	-	54.6	-	nS
Fall Time	t_f	$V_{DS}=-20V, V_{GS}=-10V, I_{DS}=-10A, R_{GEN}=6.8\Omega$	-	26.6	-	nS

GATE CHARGE CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate to Source Gate Charge	Q_{gs}	$V_{DD}=-20V, I_D=-10A, V_{GS}=0$ to -10V	-	5.7	-	nC
Gate charge at threshold	$Q_{g(th)}$	$V_{DD}=-20V, I_D=-10A, V_{GS}=0$ to -10V	-	2.5	-	nC
Gate to Drain Charge	Q_{gd}	$V_{DD}=-20V, I_D=-10A, V_{GS}=0$ to -10V	-	3.9	-	nC
Switching charge	Q_{SW}	$V_{DD}=-20V, I_D=-10A, V_{GS}=0$ to -10V	-	7.2	-	nC
Gate charge total	$Q_{g 10V}$	$V_{DD}=-20V, I_D=-10A, V_{GS}=0$ to -10V	-	26.4	-	nC
	$Q_{g 4.5V}$	$V_{DD}=-20V, I_D=-10A, V_{GS}=0$ to -4.5V	-	12	-	nC
Gate plateau voltage	$V_{plateau}$	$V_{DD}=-20V, I_D=-10A, V_{GS}=0$ to -10V	-	3.4	-	V
Gate charge total, sync. FET ($Q_g - Q_{gd}$)	$Q_{g(sync)}$	$V_{DS}=0.1V, V_{GS}=0$ to -10V	-	22.5	-	nC

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Body Diode continuous forward current	I_S	$T_C=25^\circ C$	-	-	-9.9	A
Body Diode pulse current	I_{SM}	$T_C=25^\circ C$	-	-	-15.8	A
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=-10A$	-	-0.86	-1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$V_{DD}=-20V, I_F=-10A, di/dt=100A/\mu s$	-	12.1	-	nS
Body Diode Reverse Recovery Charge	Q_{rr}	$V_{DD}=-20V, I_F=-10A, di/dt=100A/\mu s$	-	5.2	-	nC

Typical Operating Characteristics



Typical Operating Characteristics (Cont.)

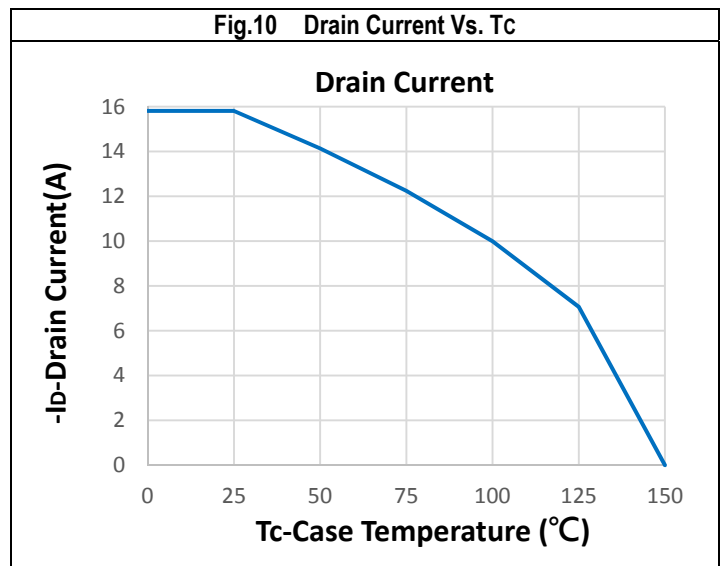
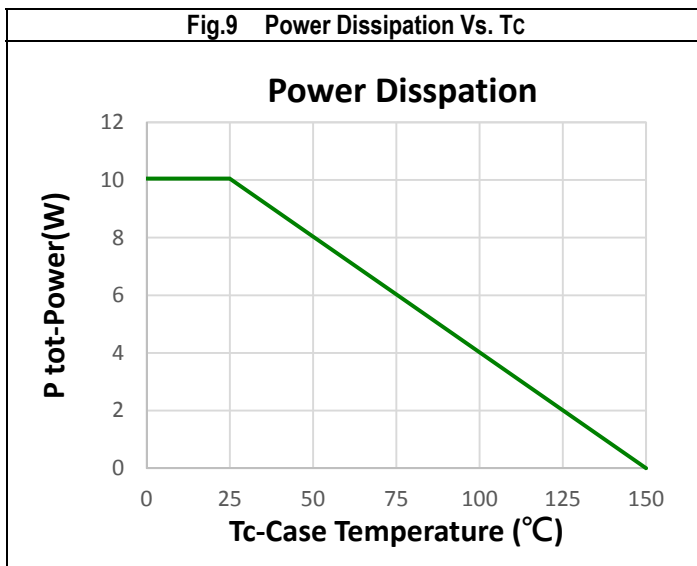
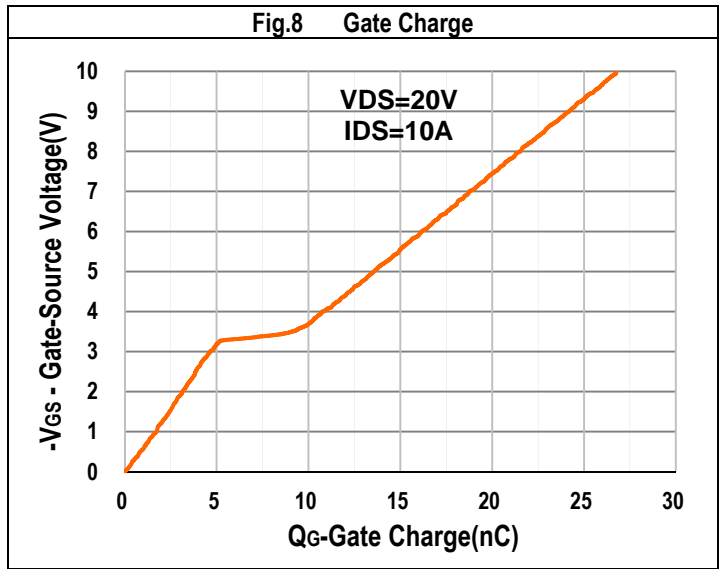
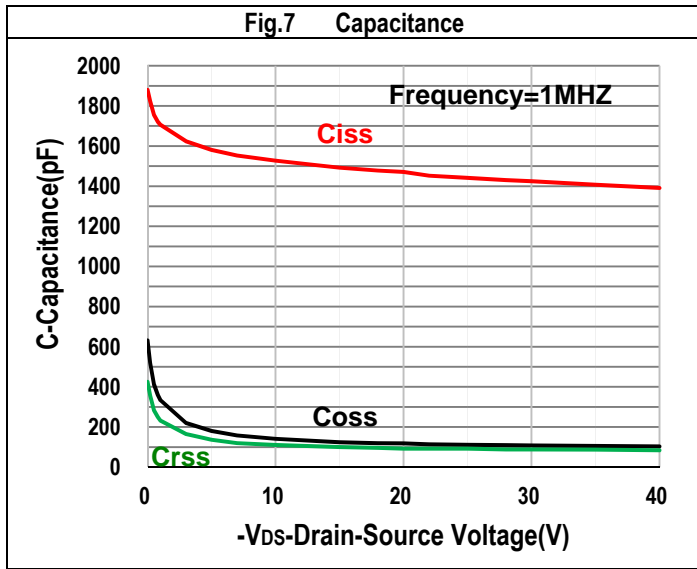


Fig.11 Safe Operation Area

Safe Operating Area

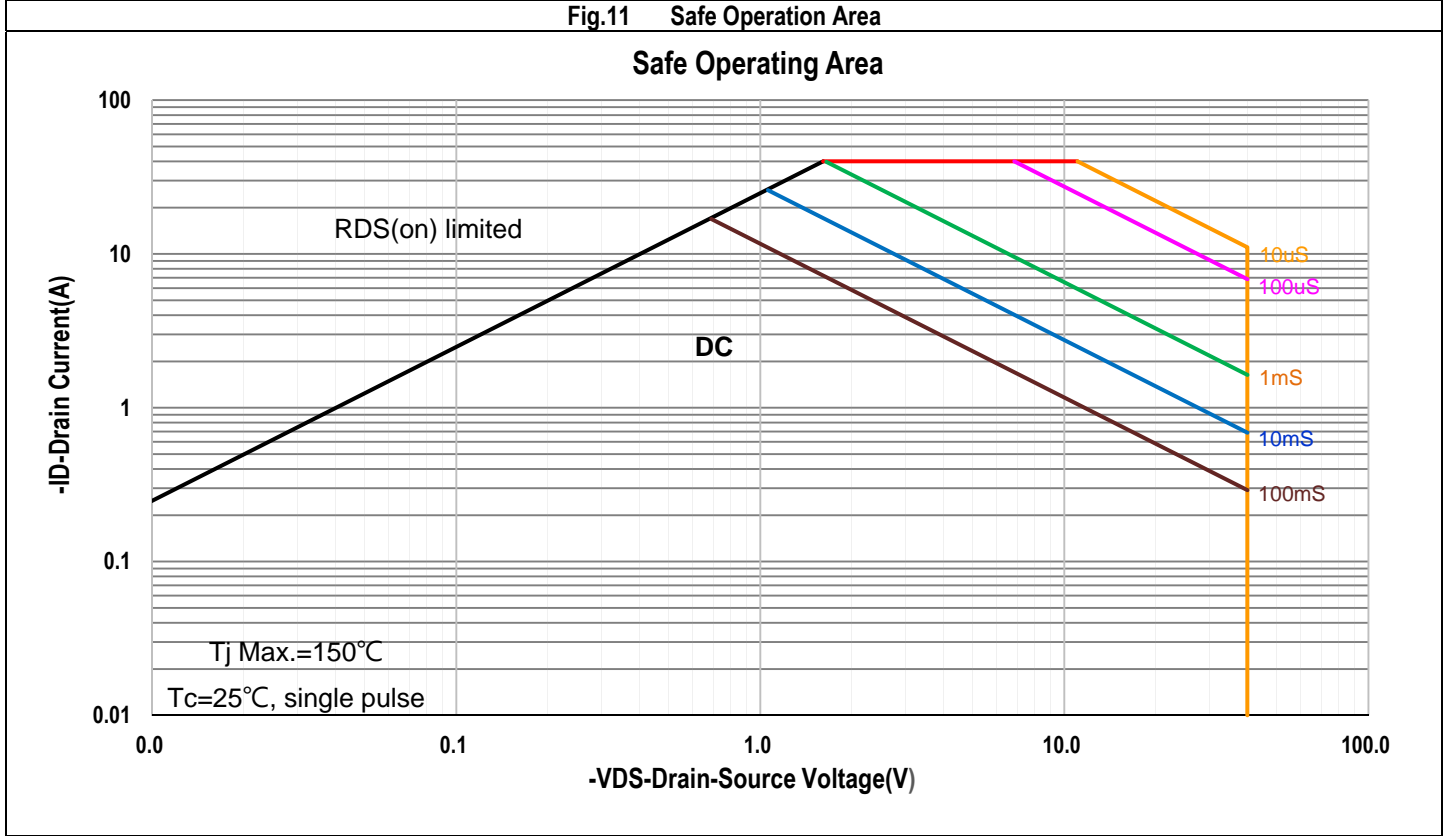
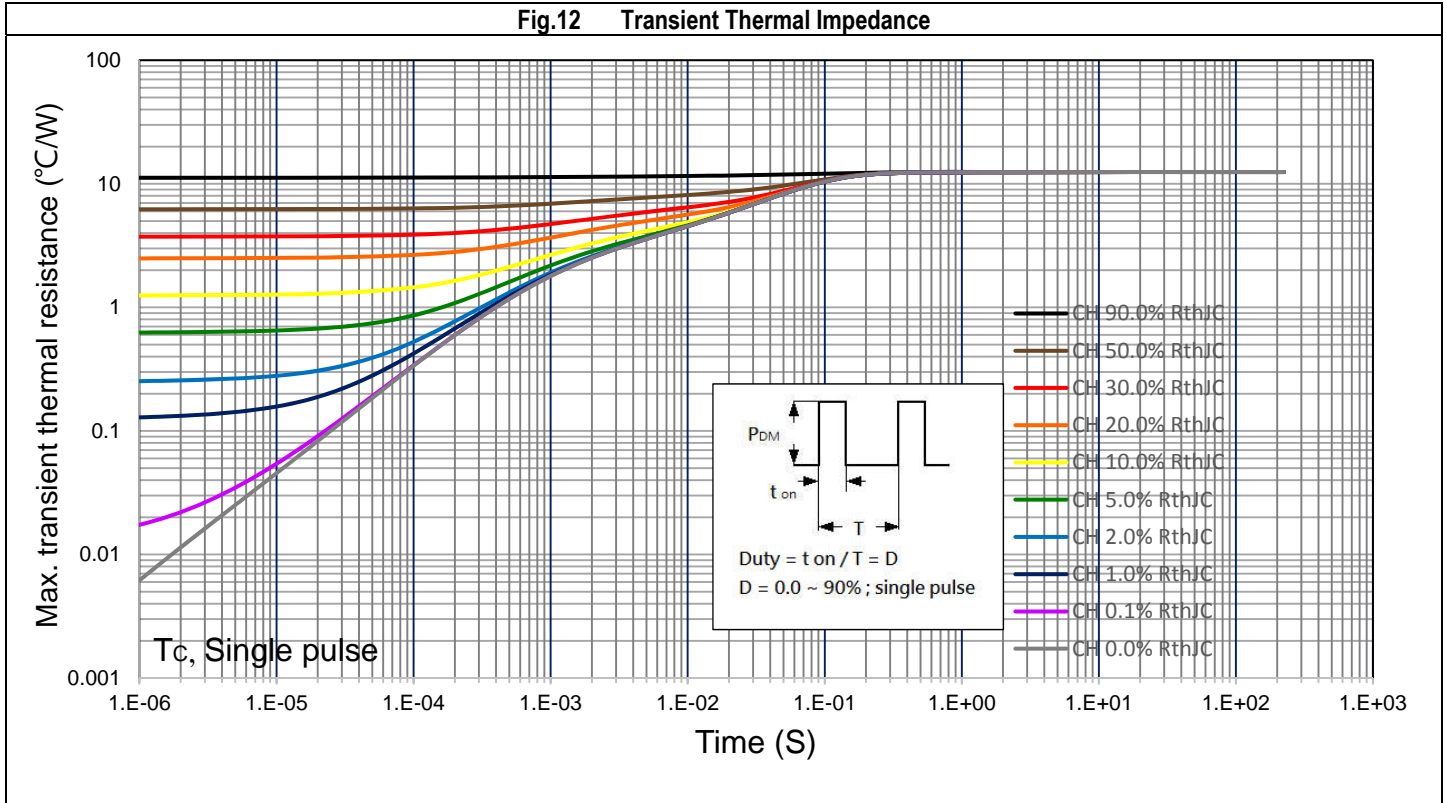
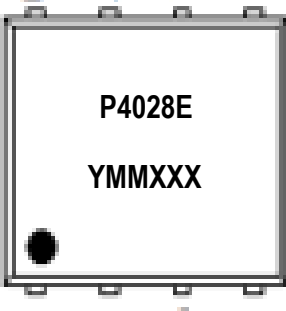


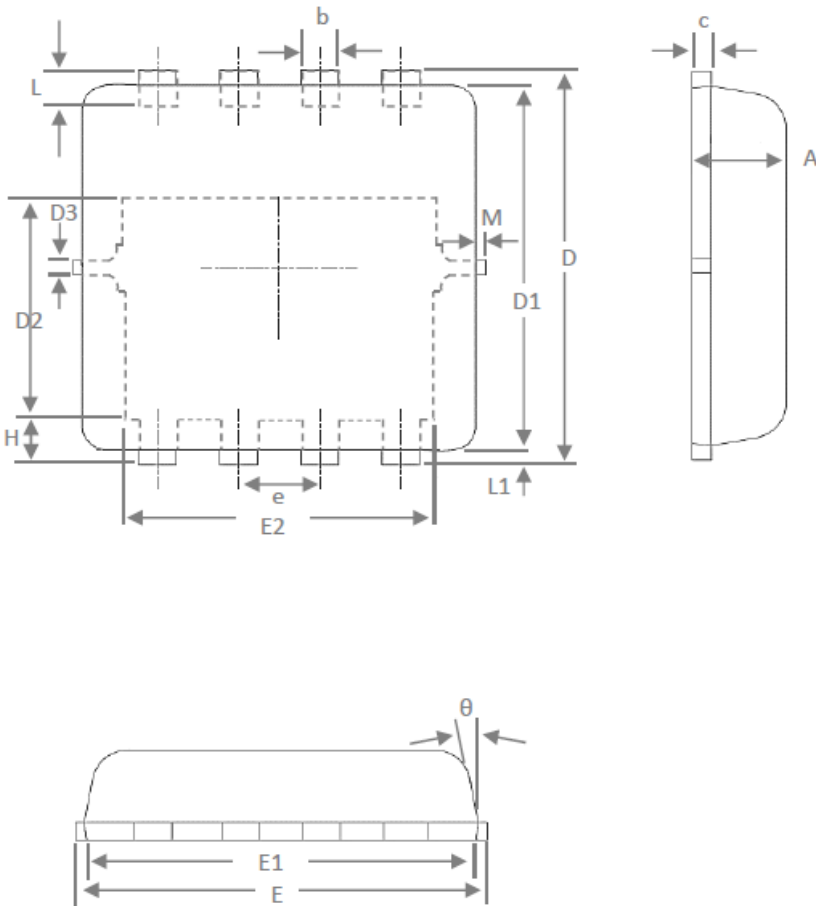
Fig.12 Transient Thermal Impedance



Marking Information

PDFN 3.3x3.3-8L (E)	Marking Rule
<p data-bbox="124 365 293 398">Laser Marking</p>  <p data-bbox="400 551 504 584">P4028E</p> <p data-bbox="395 620 512 654">YMMXXX</p> <p data-bbox="400 824 504 857">Diagram</p>	<p data-bbox="807 365 1070 398"><u>Line 1</u> : Device Name</p> <p data-bbox="807 412 903 445">P4028E</p> <p data-bbox="807 459 1038 492"><u>Line 2</u> : Date Code</p> <p data-bbox="807 506 924 539">YMMXXX</p> <p data-bbox="807 589 983 622">Y : Year Code</p> <p data-bbox="807 636 1027 669">MM : Month Code</p> <p data-bbox="807 683 1062 716">XXX : Serial Number</p>

Package of Dimension

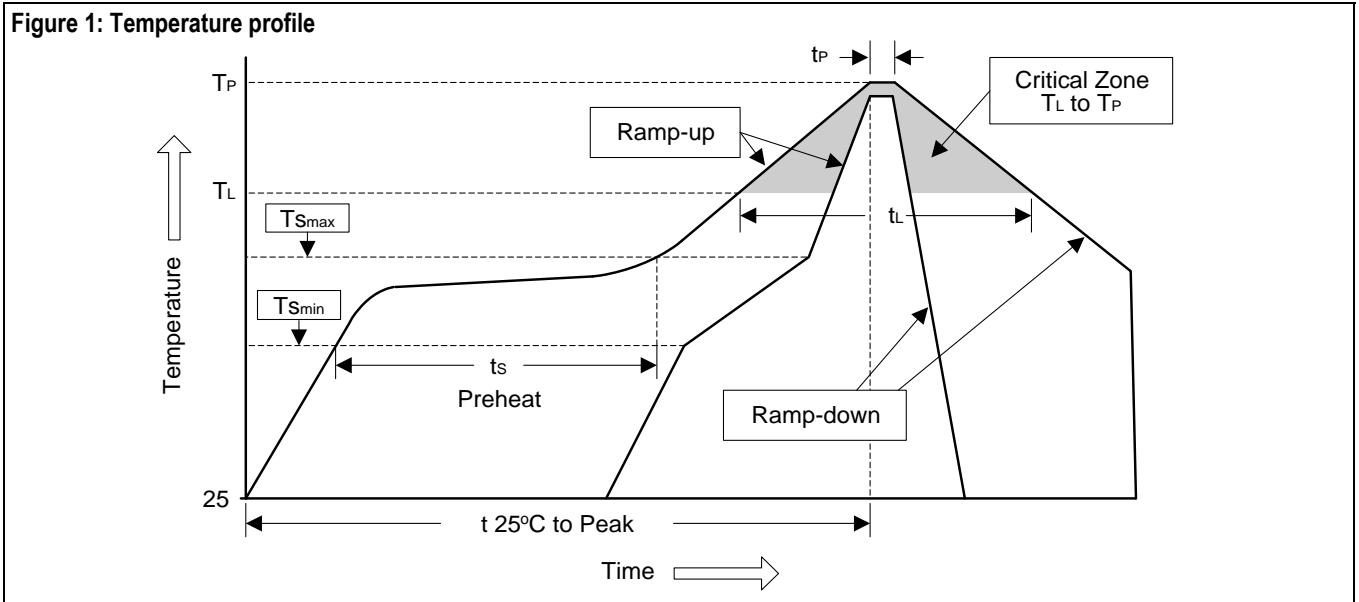


Symbol	Min	Nor	Max
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	-	0.13	-
E	3.00	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
θ	-	10°	12°
M	-	-	0.15

Soldering Methods for Silicongear's Products

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60 to 120 sec	60 to 180 sec
T_{Smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

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Revision History

Document Revision History			
Device	Date	Revision	Description (major change from last revision)
SGP4028E	Apr -14-2023	02	1. 新增 Package of Dimension 2. Marking Information 資訊修改 3. 第一頁 Q_{sw} 修正